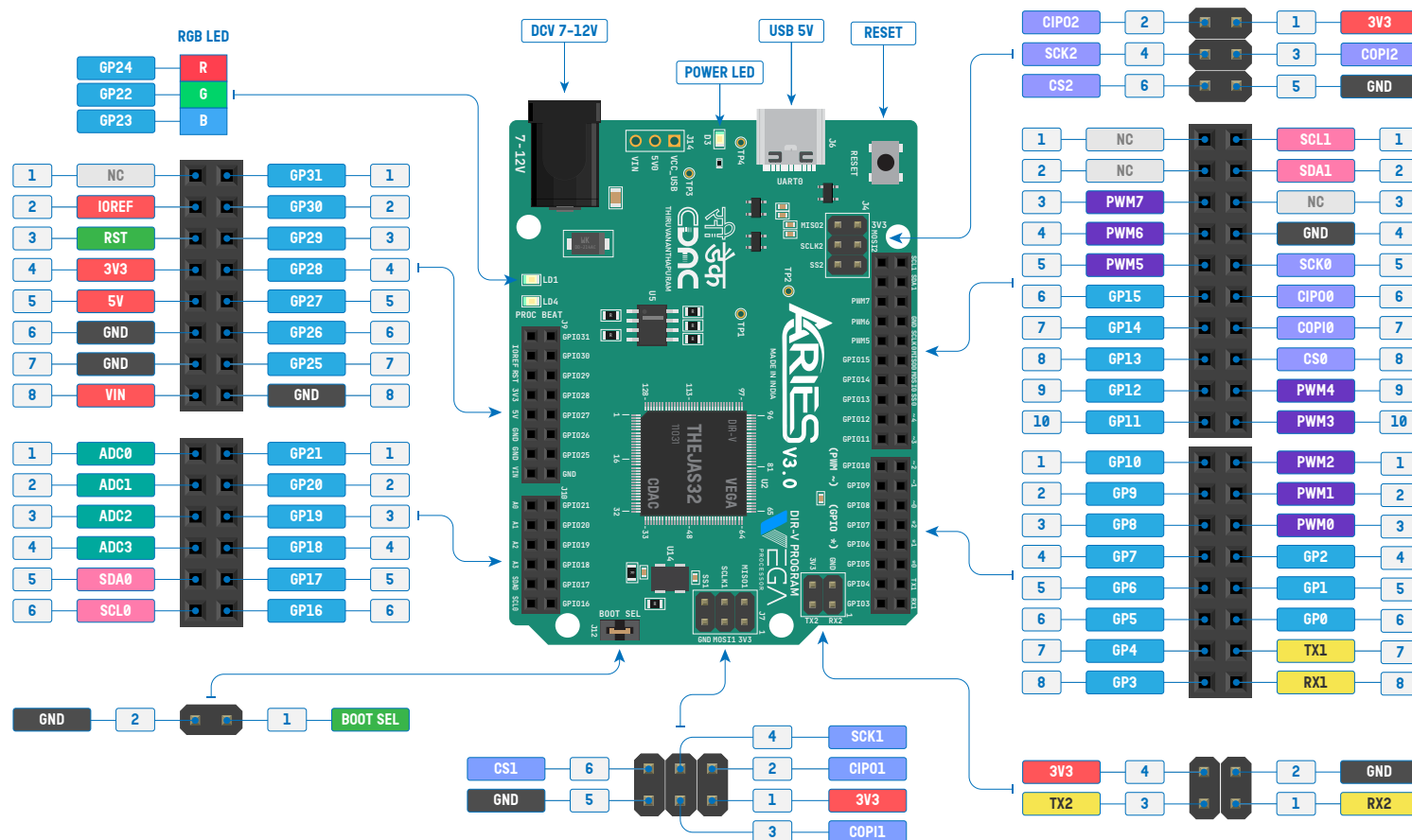


C-DAC ARIES V3 PINOUT



PHYSICAL PIN

POSITIVE SUPPLY

UART

PWM

RESET/CONTROL

GROUND SUPPLY

I2C

ANALOG

NOT CONNECTED

GPIO PORT/PIN

SPI

- UART0 pins TX0 and RX0 are connected to USB-C connector via CP2102 USB-Serial chip.
- Default baudrate of UART0 is 115200 bps.
- If JT2 is shorted, CPU boots from SPI Flash. If not shorted, boots from UART.
- If JT2 is shorted, VEGA FLASHER is selected. If not shorted, VEGA XMODEM is selected for programming.



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